

ORGANIZING COMMITTEE

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Dr.S.Ramachandran

Principal, SAEC

Chair

Dr.B.R.Tapas Babu, M.E., Ph.D

Head of the Department/ECE, SAEC

Coordinators

Dr.R.Radhika Prof./ECE

Mr.M.Maria Rubiston, Assistant Professor/ECE

Ms.M.Madhini, Assistant Professor /ECE

Mailing Address:

Dr.R.Radhika,M.E.,Ph.D.,

Course Coordinator,

National level hands on Workshop on

“Research Perspective on Full custom and semi custom design using Cadence EDA ”

Professor,

Department of ECE

S.A. Engineering College,

Poonamallee –Avadi Main Road

Thiruverkadu,Chennai – 600 077.

Tamilnadu,India.

Mobile : 9445206300

Mail: drradhika@saec.ac.in

Registration Fees: Rs.500/-

DD should be taken in favor of “The Principal (R&D), SAEC” payable at Chennai

VISION

To achieve overall excellence in education, by continuously upgrading the teaching learning process, and incorporating latest technological advancements happening worldwide with ethical responsibilities.

MISSION

- To impart sound diversified technical competency and quality education for students to enhance the employability and ethical values.
- To provide conducive environment for faculty and students with excellent facilities to improve research activity.

Topics of the Programme

Day-1: 24.08.2023

Session-1: (10 am - 1 pm)

- Introduction to Semi-Custom IC Design Flow
- Cadence Solutions for Semi-Custom IC Design
- Functional Verification using Incisive
- RTL Synthesis using Genus Synthesis Solution

Session-2: (2 pm - 4 pm)

- Physical Implementation using Innovus that includes
- Power Analysis
- Parasitic Extraction
- Generation of GDSII

Day-2: 25.08.2023

Session-1: (10 am - 1 pm)

- Introduction to Full Custom IC Design Flow
- Cadence Solutions for Custom IC Design
- Schematic Capture using Virtuoso Schematic Editor
- Symbol Creation
- Test-bench Creation using Virtuoso Schematic Editor
- Functional Simulation using Spectre

Session-2: (2 pm - 4 pm)

- Layout Design using Virtuoso Layout Editor
- Physical Verification which includes DRC & LVS
- Parasitic Extraction using Quantus
- Post Layout Simulation
- Generation of GDSII

Valedictory

*Speakers for the workshop are from
Entuple Technologies*

National level Hands on Workshop
on

“Research Perspective On Full
Custom and Semi Custom Design
Using Cadence EDA”

24.08.2023 to 25.08.2023

In Association with
Entuple Technologies



Organized by
**Department of Electronics and
Communication Engineering**



S.A. ENGINEERING COLLEGE

(Accredited by NBA,NAAC with “A” Grade and ISO 9001:2008
Certified Institution)

Poonamallee –Avadi Main Road
Thiruverkadu, Chennai – 600 077.
Tamilnadu, India.

Phone : 044 -2680 1999, 2680 1499

Fax : 044 -2680 1899

Web : www.saec.ac.in

About SAEC

S.A. Engineering College, an outcome of Dharma Naidu Educational & Charitable Trust was established in the year 1998. College has spacious, high infrastructure and peaceful surroundings with 4 lakhs sq.ft of constructed area. The campus comprises aesthetically designed buildings that are networked by fiber optic cables and Wi-Fi. SAEC has emerged as one of the premier professional institutions in this region within a short span of time. The college maintains a high standard of education by providing a wide array of world class academic facilities, employing highly qualified and experienced faculty members and creating an ambience conducive for quality education. The college offers 11 UG courses in Engineering and 6 PG courses, affiliated to Anna University and approved by AICTE. The Institution is accredited by NBA and NAAC with 'A' grade.

FOUNDER

(Late) Shri.D.Sudharssanam

BOARD OF TRUSTEES

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Shri.D.Paranthaman, Vice Chairman

Shri.D.Dasarathan, Secretary

Shri.S.Amarnaath, Treasurer

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Mrs. Saraswathi, Director, SV school

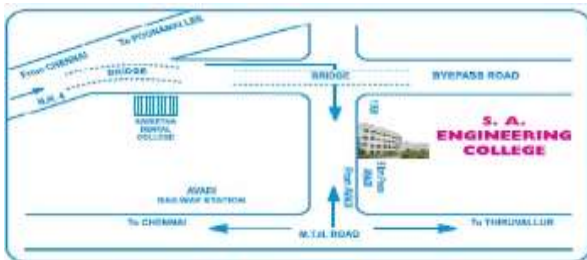
Dr.S.Aravind, Director, SACAS

Shri.D.Sabarinath, Director, SAEC

Shri. P.Venkateshraj, Correspondent,

SACAS & SV School

LOCATION



About the Organizing Department

The Department of Electronics and Communication Engineering was established in the year of 1998 with an aim of creating quality Engineers with sound technical knowledge and awareness of the latest advancements in existing and emerging technologies. Major highlights in the department include "Centre of Excellence in Automation and Robotics", and IoT research lab for green technology and telemedicine applications. Department of ECE is very keen in undertaking research projects and consultancy projects from organization like ISRO, CVRDE, etc. The department is recognized research centre of Anna University, Chennai.

About the workshop

This program is intended to inculcating young minds to do research and involve them in technological advancement in Integrated circuit design and fabrication. By combining integrated circuit (IC) design theory with real-world experience, the Commercial IC Design Program accelerates the development of marketable skills that students need before entering their local electronics ecosystem. It also exposes universities to state-of-the-art design methodologies. The program focuses on two design disciplines: digital IC design and analog/mixed-signal design. After practical lectures and product training sessions, students apply the technology and various implementation approaches to a real-world project where they function as a junior design engineer on a team working under a technical lead. Seeing a real project go from concept to design is a valuable experience that greatly reduces the ramp-up phase for new hires. The courses are ideally offered in tandem with Master's work in electrical engineering, but can also be delivered as a standalone post-bachelor's degree program.

Hence the proposed workshop focuses on cadence tool exposure On Full Custom and Semi Custom Design.

National level Hands on Workshop on "Research Perspective On Full Custom and Semi Custom Design Using Cadence EDA"

24.08.2023 to 25.08.2023

**In Association with
Entuple Technologies**

Registration Form

1. Name :
2. Qualification :
3. Designation :
4. Experience :
5. Name of the Institution with Address:
6. E-mail :
7. Mobile :

Signature of the Applicant

Certificate from Sponsoring Institute

Mr./Ms./Dr.-----
is an employee/student of our institution and he / she is sponsored to attend the above Seminar.

Signature of Sponsoring
Authority with office Seal

Date: