

## ORGANIZING COMMITTEE

### Chief Patron

**Dr.S.Ramachandran**

Principal, SAEC

### Patron

**Dr.B.R.Tapas Bapu**

Head of the Department/ECE, SAEC

### Convener

**Dr.R.Radhika Professor./ECE**

### Co-Conveners

**Mr.M.Maria Rubiston, Assistant Professor/ECE**

**Ms.G.Vasumathi, Assistant Professor/ECE**

**Ms.M.Madhini, Assistant Professor /ECE**

### Mailing Address:

**Dr.R.Radhika,M.E.,Ph.D.,**

Convener,

### National level workshop on

*“Explore the recent EDA tools and methodologies for efficient VLSI circuit design”*

Department of ECE

S.A. Engineering College,

Poonamallee –Avadi Main Road

Thiruverkadu,

Chennai – 600 077.

Tamilnadu,India.

Mobile : 9445206300

Mail: drradhika@saec.ac.in

**Registration fees:Rs.500/-**



*Scan me for payment*

### VISION

To achieve overall excellence in education, by continuously upgrading the teaching learning process, and incorporating latest technological advancements happening worldwide with ethical responsibilities.

### MISSION

To impart sound diversified technical competency and quality education for students to enhance the employability and ethical values.

To provide conducive environment for faculty and students with excellent facilities to improve research activity.

## Topics of the Programme

### Inauguration

- Micro to Nano-A Journey of Inspiring Creations(ICs)
- Recent trends in VLSI Design
- Full Custom IC design Flow of Digital Circuit-Cadence
- Introduction to FPGA Architecture
- Evolutionary algorithm for FPGA placement Routing
- Physical Design Flow
- Full Custom IC design Flow of Analog Circuit-Cadence
- Device Modeling
- Digital ASIC Design Flow
- Basic experience with Xilinx vivado Design software suite
- System on Chip Design
- Green VLSI: Energy-Efficient and Environmentally Friendly Designs
- Digital Applications- Xilinx Vivado
- Digital ASIC Design Flow
- VLSI Testing and Validation
- Exploring the PYNQ Frame work
- Demo of Python Productivity on PYNQ-Z2 Board

### Valedictory

*Speakers for the seminar are from Public, private sectors and renowned Institutions.*

## Registration Link

<https://forms.gle/Et84S4toW3jqa36L8>



## Important Dates

Submission of filled in application : April 4<sup>th</sup> 2024

Intimation of Selection : April 9<sup>th</sup> 2024

Confirmation of participants via E-mail : April 11<sup>th</sup> 2024

## One-week Hybrid Mode - National level Workshop on

**“Explore the Recent EDA Tools and Methodologies for Efficient VLSI Circuit Design”**

**15-04-2024 to 22-04-2024**

*In Association with  
Entuple Technologies Pvt.Ltd  
&  
CoreEL Technologies*



### Convener

**Dr.R.Radhika, Prof./ECE**

### Co Conveners

**Mr.M.Maria Rubiston, Asst. Prof./ECE**

**Ms.G.Vasumathi, Asst. Prof./ECE**

**Ms.M.Madhini, Asst. Prof./ECE**

### Organized by

Department

of

**Electronics and Communication Engineering**

(Accredited by NBA)



## S.A. ENGINEERING COLLEGE

(An Autonomous Institution, Affiliated to Anna University, Chennai)  
Accredited by NAAC with “A” Grade and ISO 9001:2015 Certified Institution

Poonamallee –Avadi Main Road

Thiruverkadu, Chennai – 600 077.

Tamilnadu, India.

Phone : 044 -2680 1999, 2680 1499

Web : www.saec.ac.in

## About SAEC

S.A. Engineering College, an outcome of Dharma Naidu Educational & Charitable Trust was established in the year 1998. College has spacious, high infrastructure and peaceful surroundings with 4 lakhs sq.ft of constructed area. The campus comprises aesthetically designed buildings that are networked by fiber optic cables and Wi-Fi. SAEC has emerged as one of the premier professional institutions in this region within a short span of time. The college maintains a high standard of education by providing a wide array of world class academic facilities, employing highly qualified and experienced faculty members and creating an ambience conducive for quality education. The college offers 10 UG courses in Engineering and 6 PG courses, affiliated to Anna University and approved by AICTE. The Institution is accredited by NBA and NAAC with 'A' grade.

### FOUNDER

**(Late) Shri.D.Sudharssanam**

### BOARD OF TRUSTEES

**Shri.D.Duraiswamy, Chairman**

**Shri.D.Paranthaman, Vice Chairman**

**Shri.D.Dasarathan, Secretary**

**Shri.S.Amarnaath, Treasurer & Correspondent**

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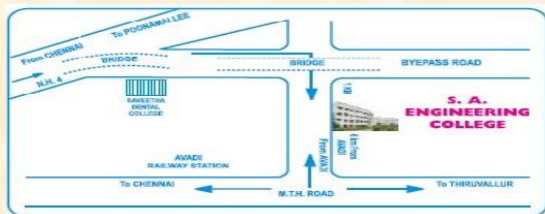
**Mrs.D. Saraswathi, Director, SV school**

**Dr.S.Aravind, Director, SACAS**

**Shri.D.Sabarinath, Director, SAEC**

**Shri. P.Venkateshraj, Correspondent, SACAS & SV School**

### LOCATION



## About the Organizing Department

The Department of Electronics and Communication Engineering was established in the year of 1998 with an aim of creating quality Engineers with sound technical knowledge and awareness of the latest advancements in existing and emerging technologies. Major highlights in the department include "Centre of Excellence in Automation and Robotics", and IoT research lab for green technology and telemedicine applications. Department of ECE is very keen in undertaking research projects and consultancy projects from organization like ISRO, CVRDE, etc. The department is recognized research centre of Anna University, Chennai.

## About the Workshop

In the fast-evolving landscape of VLSI circuit design, the integration of advanced Electronic Design Automation (EDA) tools and methodologies has become paramount to achieving optimal performance, reliability, and efficiency. This exploration focuses on the latest trends shaping the field, offering insights into the cutting-edge tools and methodologies that drive innovation in Very Large Scale Integration (VLSI) design. Exploring recent Electronic Design Automation (EDA) tools and methodologies is crucial for staying abreast of advancements in VLSI (Very Large Scale Integration) circuit design. EDA tools play a vital role in automating various stages of the design process, enhancing productivity, and ensuring the reliability of complex integrated circuits. This exploration underscores the pivotal role of EDA tools and methodologies in shaping the VLSI design landscape. The continuous integration of advanced tools and methodologies not only enhances productivity but also addresses the challenges posed by increasing design complexity. Staying informed and adopting these innovations is crucial for designers and engineers striving to push the boundaries of VLSI circuit design in the new era.

**Hence the proposed workshop focuses on the topics related to wide advances about *Explore the Recent EDA Tools and Methodologies for Efficient VLSI Circuit Design.***

## One week Hybrid Mode - National level Workshop on “Explore the Recent EDA Tools and Methodologies for Efficient VLSI Circuit Design ”

**15-04-2024 to 19-04-2024**

*In Association with*

*Entuple Technologies Pvt.Ltd  
&  
CoreEL Technologies*

## Registration Form

1. Name :
2. Qualification :
3. Designation :
4. Experience :
5. Name of the Institution with Address:
6. E-mail :
7. Mobile :

Signature of the Applicant

## Certificate from Sponsoring Institute

Mr./Ms./Dr.----- is an employee/student of our institution and he / she is sponsored to attend the above Seminar.

Signature of Sponsoring  
Authority with office Seal

Date: